

## Features

- 8-bit CPU, ROM, RAM, I/O in a DIP 40 package
- 4 analog outputs with 6 bit resolution
- 30 digital I/O lines
  - two serial interfaces
  - two 8-bit interfaces
  - two 4-bit interfaces
  - two test inputs
- 2 Kbyte ROM
- 64 byte RAM
- 7.5  $\mu$ s cycle time at 4 MHz crystal frequency – 1 or 2 cycles per instruction
- Zero passage detector
- Interface for modulated digital signal
- Interval timer/counter
- 5 V supply voltage
- SAB 8048 instruction subset